

XMC-18AISS6C12M

18-Bit, 6-Channel, 12 MSPS XMC Analog Input Board
With 2-Megasample Data FIFO and SAR Converters

FEATURES:

- Six Differential Analog Inputs with dedicated 18-Bit ADC per channel
- True Simultaneous Sampling of all inputs from Zero to 12MSPS per channel
- 2.0 Megasample input data FIFO buffer
- SAR Architecture; No minimum sample rate; Minimum Latency
- Selectable Input Ranges: (Order Wideband or High-Level)
Wideband: $\pm 2.5V$, $\pm 1.25V$, $\pm 0.625V$; or High-Level: $\pm 10V$, $\pm 5V$, $\pm 2.5V$
- Miniature MMCX coaxial system connectors for analog inputs and timing I/O.
- Hardware Clock and Sync I/O for multiboard operation
- Timing Controlled by internal rate generators, by software clocking, or externally
- DMA Engine minimizes bus congestion
- Dual Power Conversion for optimum internal power voltage integrity
- Internal on-demand Autocalibration of all channels
- PCI Express control interface
- XMC form factor
- Extended-temperature version available

Call for PCI Express and CPCI form factors

TYPICAL APPLICATIONS:

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|--------------------------|------------------|---------------------|
| ✓ Wideband Analog Inputs | ✓ Event Capture | ✓ Imaging Systems |
| ✓ Dynamic Test Systems | ✓ Crash Analysis | ✓ Spectrum Analysis |

REV: 011918

**** **IN DEVELOPMENT** ****