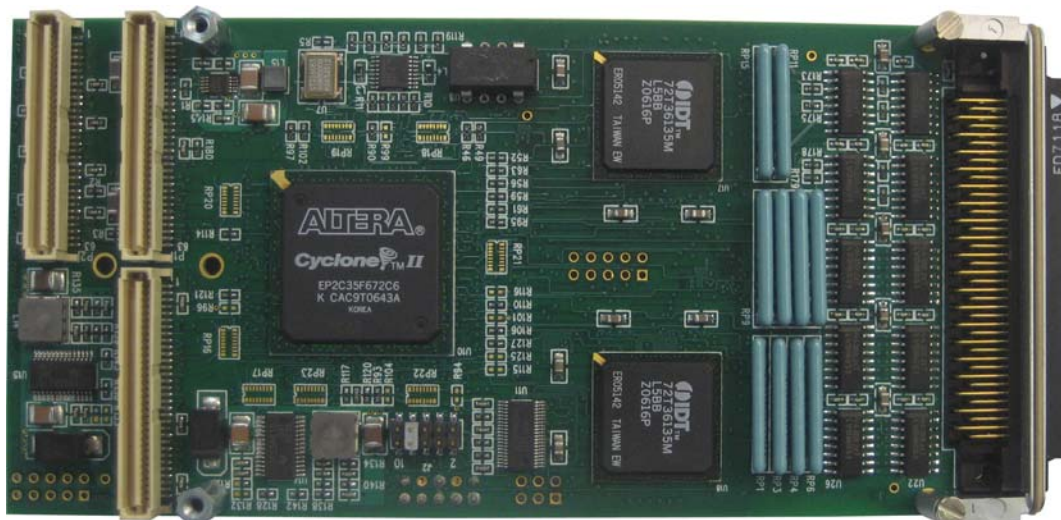


*** * * * * PRELIMINARY * * * * ***
General Standards Corporation
High Performance Bus Interface Solutions
PMC64C-HPDI40C2L
High-speed 40-Bit Parallel Digital I/O PCI Board



Features Include:

- . 400 Mbytes per second I/O transfer rate via the front panel connector (LVDS transceivers).
- . 400 Mbytes per second PCI transfer rate in burst mode (64bit 66MHz).
- . Can interface to a wide variety of external high-speed devices.
- . "Deep FIFO buffers" (4 Megabytes) allow data bursts to be transferred over the PCI bus.
- . 64-Bit data transfers on the PCI bus.
- . On-board cable controller, FIFOs, and DMA engine provide for continuous data transfer.
- . Data input/output clock rate up to 100 MHz.
- . 64-Bit, 66MHz PCI v2.2 compliant.
- . "Program-and-forget" DMA engine handles Direct DMA and Scatter Gather DMA transfers.
- . Cable data input/output width of 32 bits.
- . 8 bi-directional signals can be used as clocks and/or handshaking protocol.

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FAX: (256)880-8788
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