

General Standards Corporation

High Performance Bus Interface Solutions

VHDx-4D25x-Sxx

VHDCI to 4x DB25 Shielded Cable



Ordering Information:

VHD<Length>-4D25<Gender>-S<Pinout><Exit>

- <Length> => Total Length of Cable, in Feet
- <Gender> => DB25 Gender
 P = Pin (Plug)
 S = Socket (Receptacle)
- <Pinout> => DB25 Pinout (See following tables)
 0 = Standard
 1 = RS530A
 2 = RS530A Reversed

Note: Pinouts refer to pinouts used for DB25s in other GSC Serial Cables

- <Exit> => VHDCI Cable Exit
 S = Straight Exit
 R = 45° Right Exit - toward Pin 1/35
 L = 45° Left Exit - toward Pin 34/68

Example: 12 ft cable, 4x DB25 Plug, "Standard" pinout, Straight Exit VHDCI

PN = VHD12-4D25P-S0S

General Standards Corporation
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Features:

- Used on PCIe4-SIO8BX2V (2 required per board)
- 30 AWG twisted pair cable with braided shield
- All connectors are shielded with backshell and jackscrews
- DB25 Connectors are labeled as Port1 - Port 4
- Straight VHDCI Connector:
 - TE 5787775-1 VHDCI Connector
 - TE 5788362-1 Shielded Backshell
 - M2 thumbscrews
- Angled VHDCI Connector:
 - Honda HDRA-E68MAI VHDCI Connector
 - Honda 191-000002-010 Shielded 45° Backshell
 - M2 thumbscrews
- DB25P (Plug) Connectors::
 - TE 1658659-1 Shielded DB25 Plug Connector with Jackscrews.
 - TE 66507-3 Pins (crimp)
- DB25S (Socket) Connectors::
 - TE 1658658-1 Shielded DB25 Receptacle Connector with Jackscrews
 - TE 66505-3 Pins (crimp)

Note: connectors may be replaced with equivalent parts, if necessary

- For cable length 4ft and over, cable is split into four DB25 cables at 2ft from DB25 Connectors.
For cable length 3ft to 4ft, cable is split into four DB25 cables at 18in from DB25 Connectors.
For cable length 2ft to 3ft, cable is split into four DB25 cables at 1ft from DB25 Connectors.
- Cables can be customized, if necessary. Consult factory for details

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Pinout Code "0" - Standard Pinout

VHDCI Pin #	RS422/RS485	DB25 Pin #
	DTE	
PORT 1		
1	AUXC1+	12
2	AUXC1-	25
3	DCD1+	10
4	DCD1-	23
5	CTS1+	3
6	CTS1-	16
7	RXD1+	1
8	RXD1-	14
9	RXC1+	5
10	RXC1-	18
11	RTS1+	9
12	RTS1-	22
13	TXD1+	7
14	TXD1-	20
15	TXC1+	11
16	TXC1-	24
17	SGND1	13
PORT 2		
18	SGND2	13
19	CTS2+	3
20	CTS2-	16
21	RXD2+	1
22	RXD2-	14
23	RXC2+	5
24	RXC2-	18
25	RTS2+	9
26	RTS2-	22
27	TXD2+	7
28	TXD2-	20
29	TXC2+	11
30	TXC2-	24
31	DCD2+	10
32	DCD2-	23
33	AUXC2+	12
34	AUXC2-	25

VHDCI Pin #	RS422/RS485	DB25 Pin #
	DTE	
PORT 3		
35	AUXC3+	12
36	AUXC3-	25
37	DCD3+	10
38	DCD3-	23
39	CTS3+	3
40	CTS3-	16
41	RXD3+	1
42	RXD3-	14
43	RXC3+	5
44	RXC3-	18
45	RTS3+	9
46	RTS3-	22
47	TXD3+	7
48	TXD3-	20
49	TXC3+	11
50	TXC3-	24
51	SGND3	13
PORT 4		
52	SGND4	13
53	CTS4+	3
54	CTS4-	16
55	RXD4+	1
56	RXD4-	14
57	RXC4+	5
58	RXC4-	18
59	RTS4+	9
60	RTS4-	22
61	TXD4+	7
62	TXD4-	20
63	TXC4+	11
64	TXC4-	24
65	DCD4+	10
66	DCD4-	23
67	AUXC4+	12
68	AUXC4-	25

Notes: All +/- are twisted pairs
Signal Names (from PCIe-SIO8BX2V in RS485 DTE Mode) are for reference only.

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Pinout Code "1" - RS530A Pinout

VHDCI Pin #	RS422/RS485	DB25 Pin #
	DTE	
PORT 1		
1	AUXC1+	12
2	AUXC1-	15
3	DCD1+	10
4	DCD1-	8
5	CTS1+	13
6	CTS1-	5
7	RXD1+	16
8	RXD1-	3
9	RXC1+	9
10	RXC1-	17
11	RTS1+	19
12	RTS1-	4
13	TXD1+	14
14	TXD1-	2
15	TXC1+	11
16	TXC1-	24
17	SGND1	7
PORT 2		
18	SGND2	7
19	CTS2+	13
20	CTS2-	5
21	RXD2+	16
22	RXD2-	3
23	RXC2+	9
24	RXC2-	17
25	RTS2+	19
26	RTS2-	4
27	TXD2+	14
28	TXD2-	2
29	TXC2+	11
30	TXC2-	24
31	DCD2+	10
32	DCD2-	8
33	AUXC2+	12
34	AUXC2-	15

VHDCI Pin #	RS422/RS485	VHDCI Pin #
	DTE	
PORT 3		
35	AUXC3+	12
36	AUXC3-	15
37	DCD3+	10
38	DCD3-	8
39	CTS3+	13
40	CTS3-	5
41	RXD3+	16
42	RXD3-	3
43	RXC3+	9
44	RXC3-	17
45	RTS3+	19
46	RTS3-	4
47	TXD3+	14
48	TXD3-	2
49	TXC3+	11
50	TXC3-	24
51	SGND3	7
PORT 4		
52	SGND4	7
53	CTS4+	13
54	CTS4-	5
55	RXD4+	16
56	RXD4-	3
57	RXC4+	9
58	RXC4-	17
59	RTS4+	19
60	RTS4-	4
61	TXD4+	14
62	TXD4-	2
63	TXC4+	11
64	TXC4-	24
65	DCD4+	10
66	DCD4-	8
67	AUXC4+	12
68	AUXC4-	15

Notes: All +/- are twisted pairs
Signal Names (from PCIe-SIO8BX2V in RS485 DTE Mode) are for reference only.

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Pinout Code "2" - RS530A REV Pinout

VHDCI Pin #	RS422/RS485	DB25 Pin #	VHDCI Pin #	RS422/RS485	DB25 Pin #
	DTE			DTE	
PORT 1			PORT 3		
1	AUXC1+	15	35	AUXC3+	15
2	AUXC1-	12	36	AUXC3-	12
3	DCD1+	8	37	DCD3+	8
4	DCD1-	10	38	DCD3-	10
5	CTS1+	5	39	CTS3+	5
6	CTS1-	13	40	CTS3-	13
7	RXD1+	3	41	RXD3+	3
8	RXD1-	16	42	RXD3-	16
9	RXC1+	17	43	RXC3+	17
10	RXC1-	9	44	RXC3-	9
11	RTS1+	4	45	RTS3+	4
12	RTS1-	19	46	RTS3-	19
13	TXD1+	2	47	TXD3+	2
14	TXD1-	14	48	TXD3-	14
15	TXC1+	24	49	TXC3+	24
16	TXC1-	11	50	TXC3-	11
17	SGND1	7	51	SGND3	7
PORT 2			PORT 4		
18	SGND2	7	52	SGND4	7
19	CTS2+	5	53	CTS4+	5
20	CTS2-	13	54	CTS4-	13
21	RXD2+	3	55	RXD4+	3
22	RXD2-	16	56	RXD4-	16
23	RXC2+	17	57	RXC4+	17
24	RXC2-	9	58	RXC4-	9
25	RTS2+	4	59	RTS4+	4
26	RTS2-	19	60	RTS4-	19
27	TXD2+	2	61	TXD4+	2
28	TXD2-	14	62	TXD4-	14
29	TXC2+	24	63	TXC4+	24
30	TXC2-	11	64	TXC4-	11
31	DCD2+	8	65	DCD4+	8
32	DCD2-	10	66	DCD4-	10
33	AUXC2+	15	67	AUXC4+	15
34	AUXC2-	12	68	AUXC4-	12

Notes: All +/- are twisted pairs
Signal Names (from PCIe-SIO8BX2V in RS485 DTE Mode) are for reference only.